

AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph on page 1, lines 12-18 with the following amended paragraph:

In recent years, there has been an increasing demand for improved processor performance in a variety of fields including multimedia processing and high-definition image processing. With the current LSI (large-scale integrated circuit) manufacturing techniques, however, there is a limit for speeding up ~~[[of]]~~ devices. Thus, parallel processing has attracted attention, and research and development of multiprocessor systems have vigorously been made.

Please replace the paragraph on page 1, lines 27-32 with the following amended paragraph:

There also exists a demand for reduced power consumption of a system for elongation of life of batteries mounted on portable equipment and for consideration for a recently advocated environmental issue ~~environment that have recently been said profusely~~. One way to reduce the power consumption of the system is to decrease a clock frequency for a part or a whole of the system according to an operating situation of the system.

Please replace the paragraph on page 2, lines 13-17 with the following amended paragraph:

In such a system, if first processor 101 does not need to execute any processing until second processor 102 completes the processing, it would issue a ~~[[cock]]~~ clock control command to clock supply control unit 105 to force it to stop the clock supply to first processor 101 so as to reduce the power consumption of first processor 101.

Please replace the paragraph on page 10, lines 2-4 with the following amended paragraph:

In step S13, if estimate Tb is greater than estimate Ta ("Yes" in S13), first processor 11 controls variable power source 16 to raise the power supply voltage being supplied to second processor 12 ~~first processor 11~~ (S34).

Please replace the paragraph on page 13, line 33 to page 14, line 3 with the following amended paragraph:

The schematic configuration of the multiprocessor system of the present embodiment is identical to that of the fourth embodiment shown in Fig. 10 ~~Fig-5~~, and therefore, detailed description of the common configurations and functions will not be repeated.